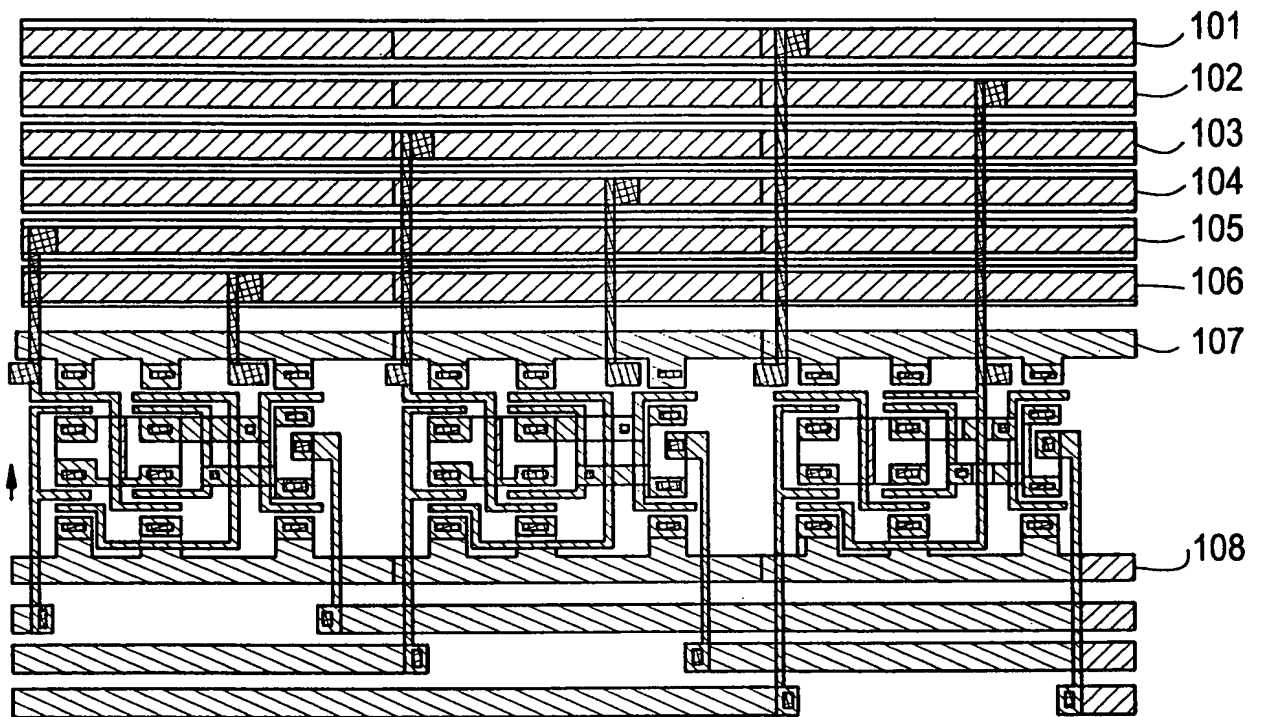
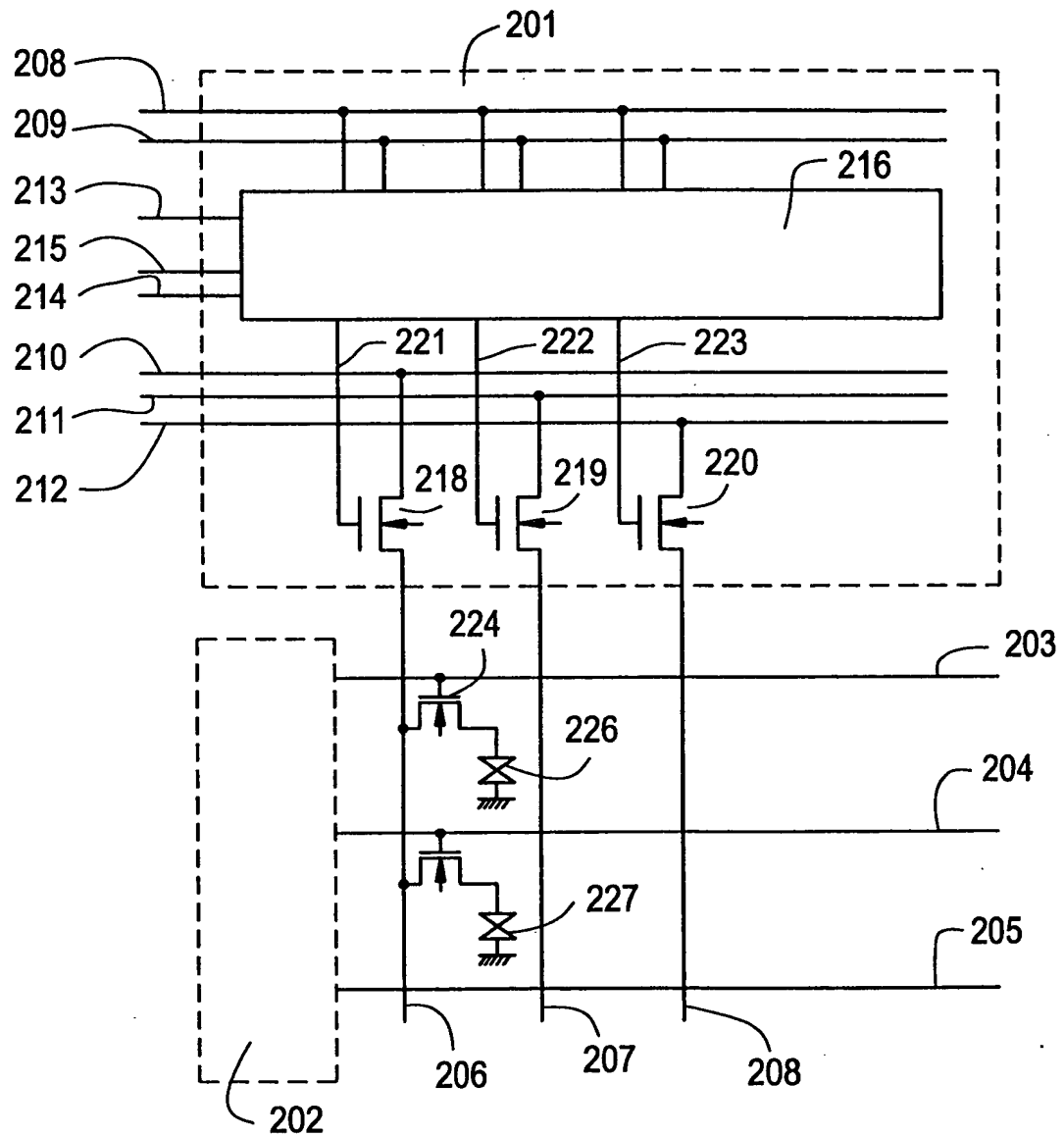


FIG. 1



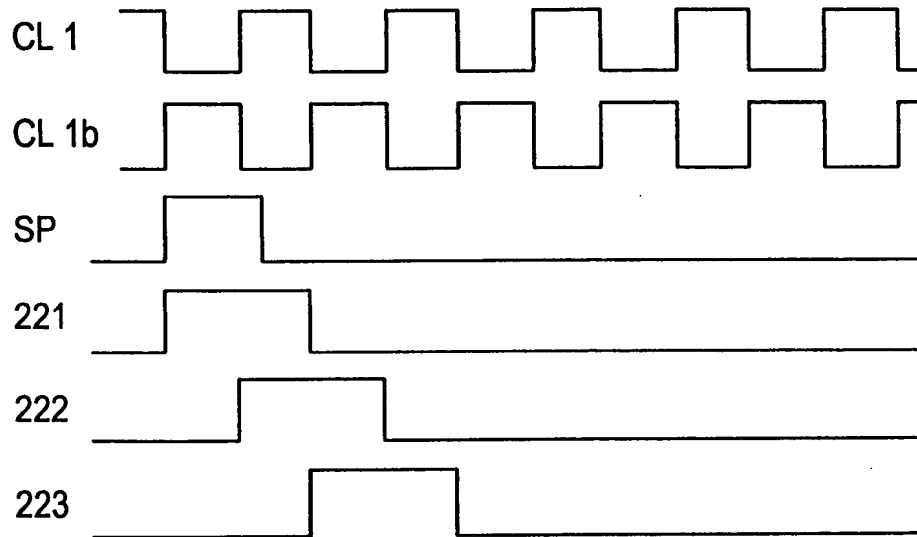
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FIG. 2
PRIOR ART



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FIG. 3
PRIOR ART



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FIG.4A
PRIOR ART

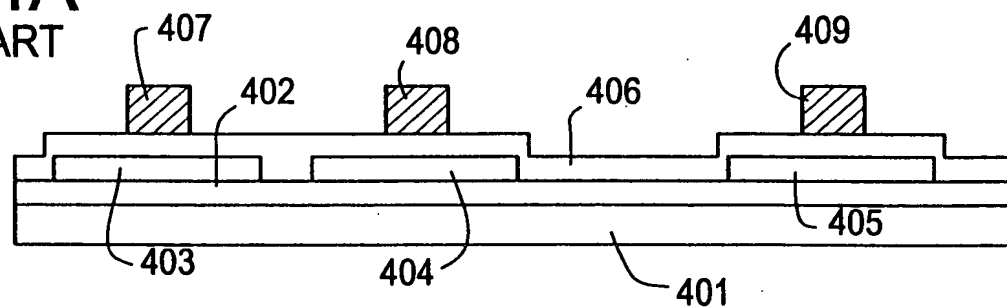


FIG.4B
PRIOR ART

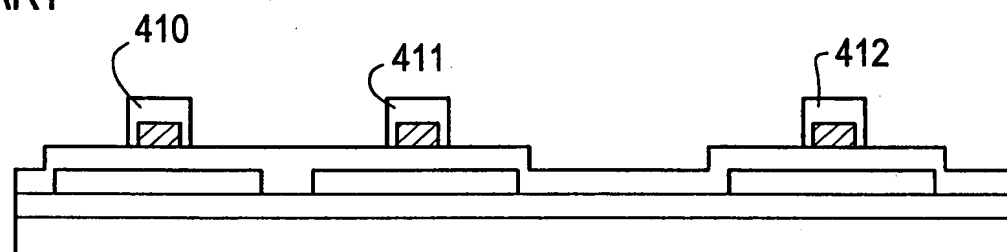


FIG.4C
PRIOR ART

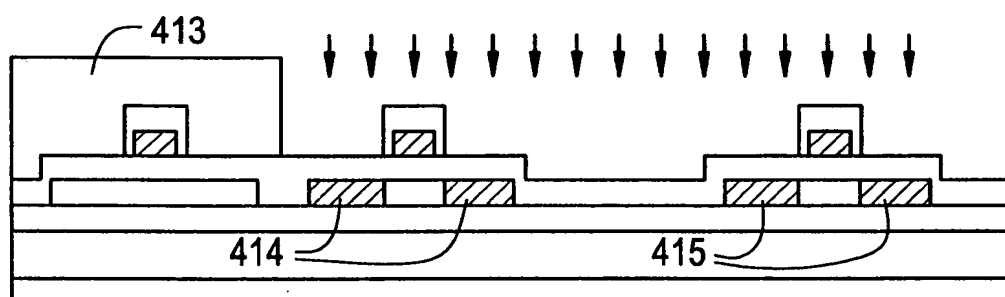


FIG.4D
PRIOR ART

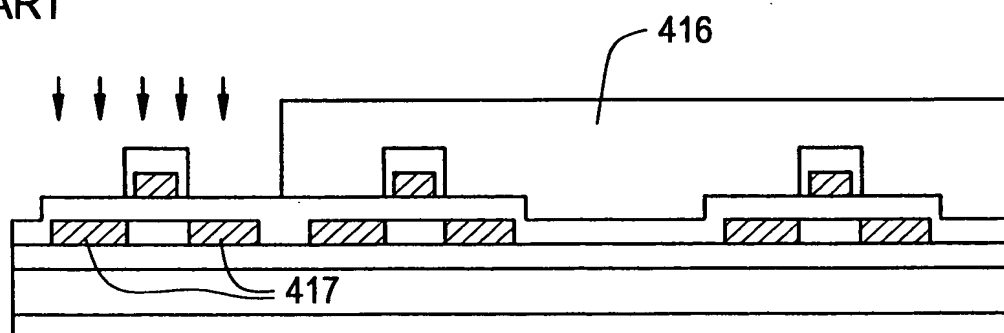


FIG. 5A
PRIOR ART

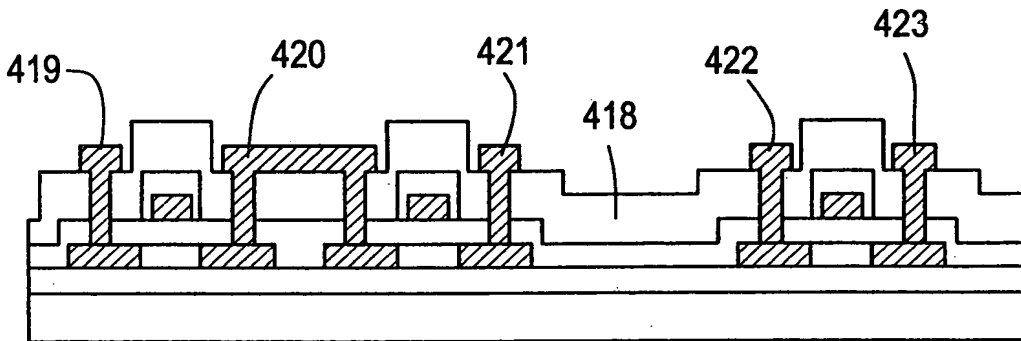
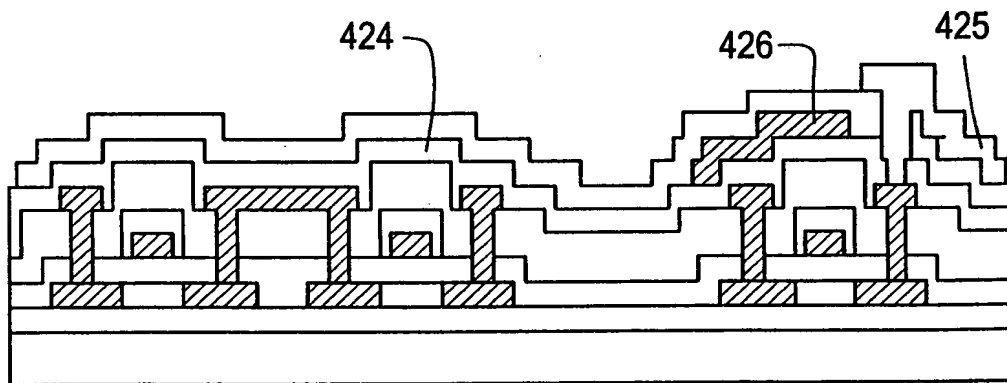
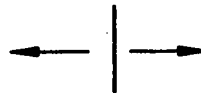


FIG. 5B
PRIOR ART

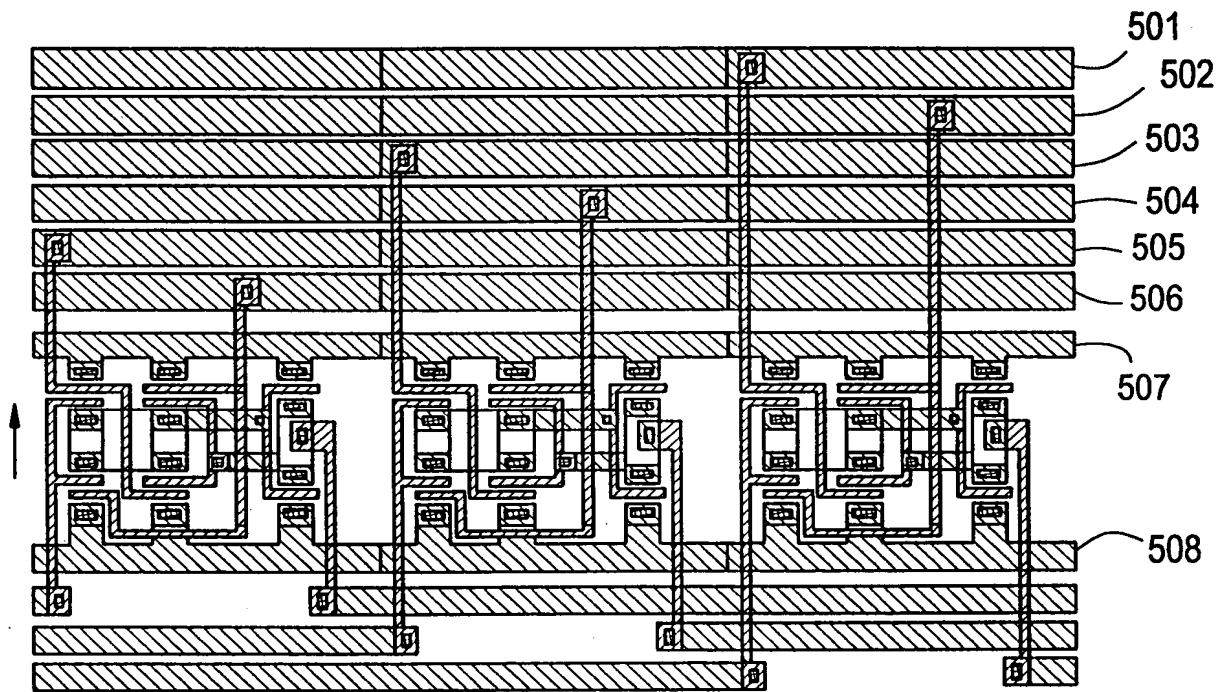


DRIVER CIRCUIT
TFT



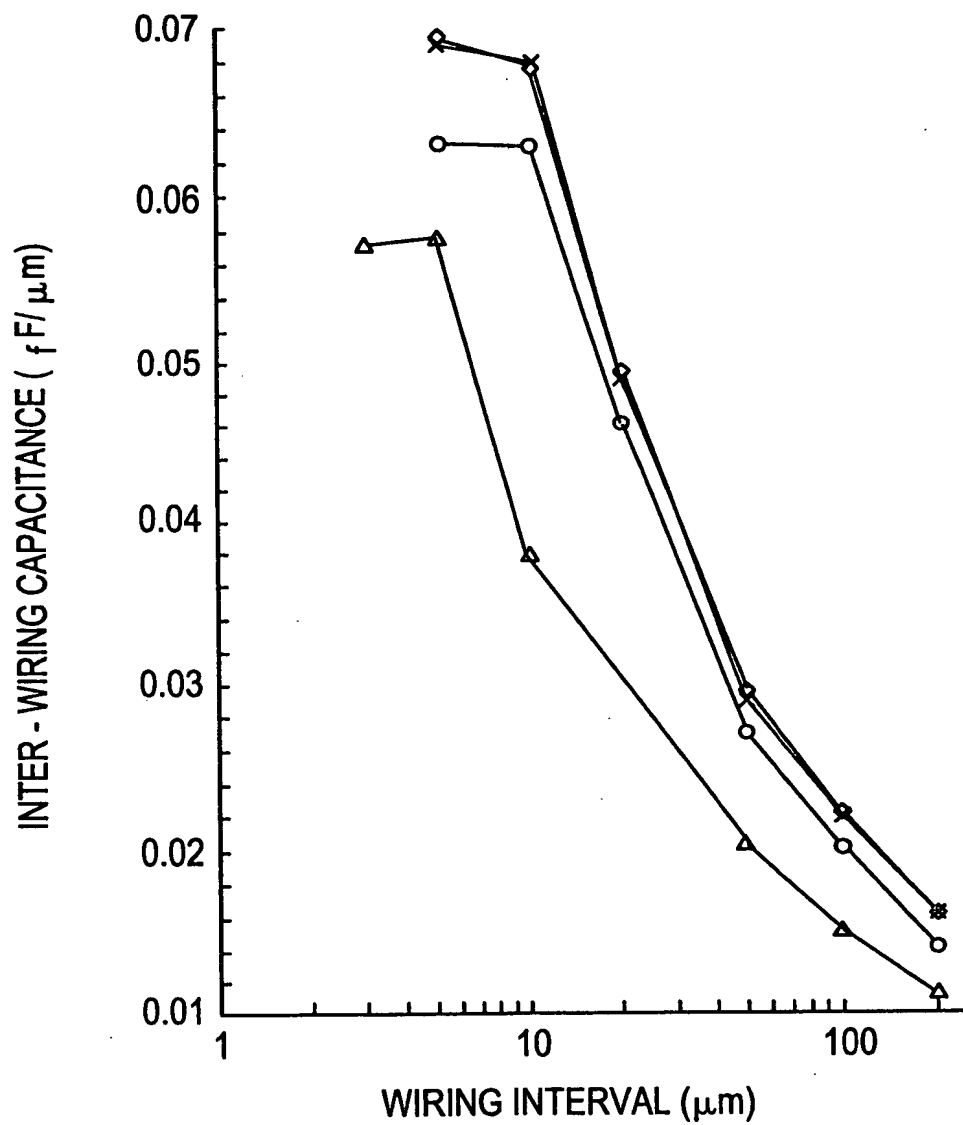
PIXEL TFT

FIG. 6
PRIOR ART



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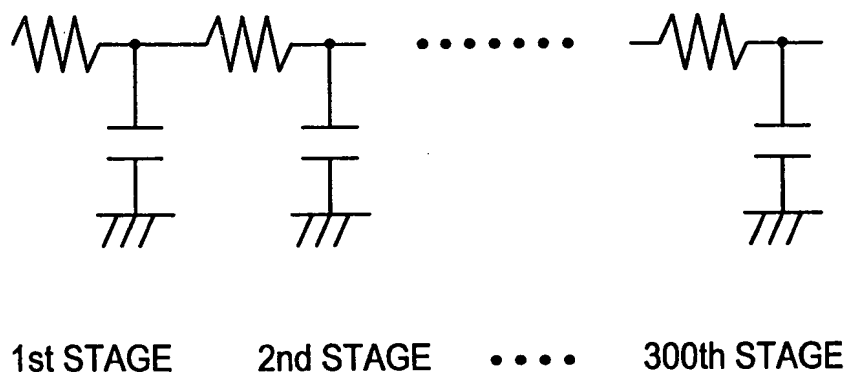
FIG. 7



- △— between Wiring A1 and Gate A1 (width 10 μ m)
- ◇— between Wiring A1 and Gate A1 (width 30 μ m)
- between Wiring A1s (width 30 μ m)
- ×— between Gate A1 s (width 30 μ m)

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FIG. 8



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FIG. 9

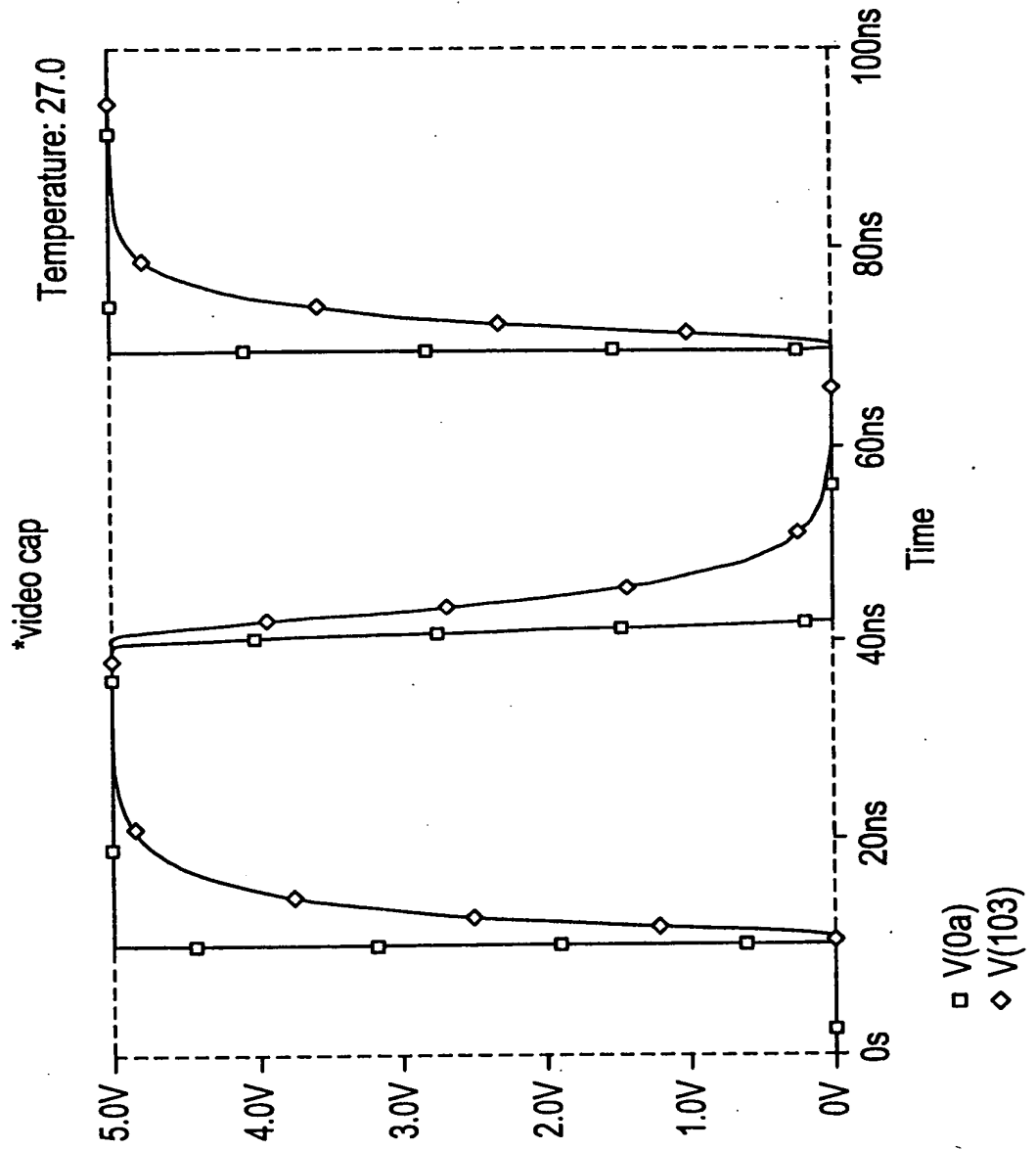
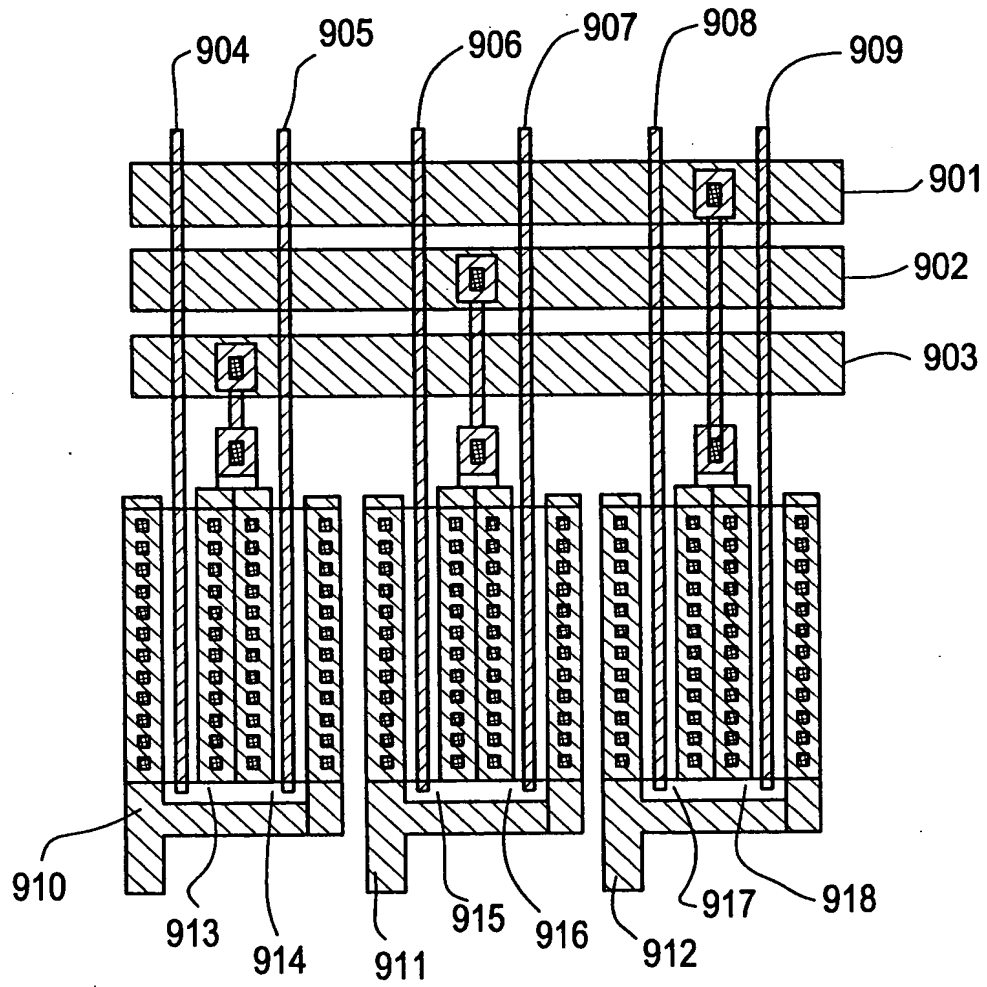


FIG. 10



TOP SECRET

FIG. 11

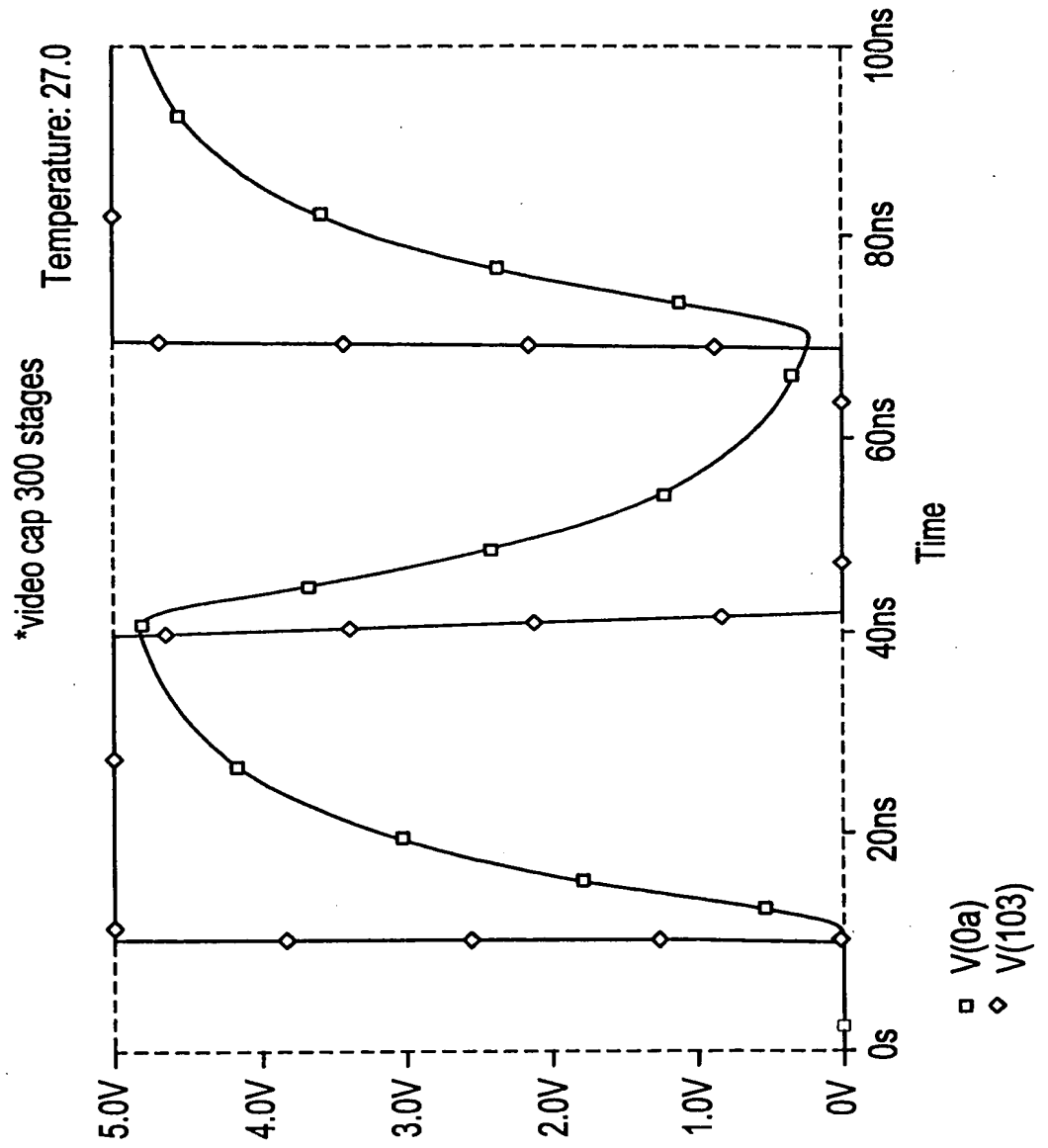


FIG. 12A

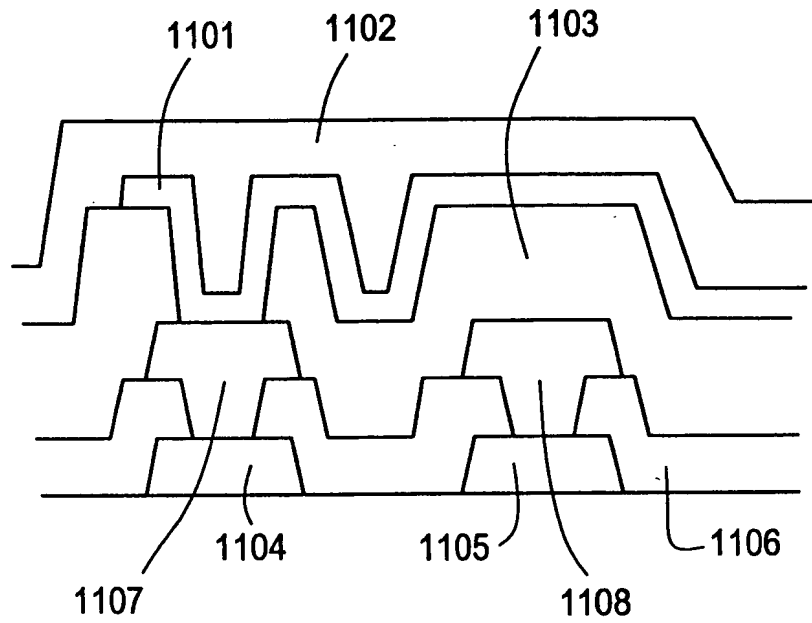
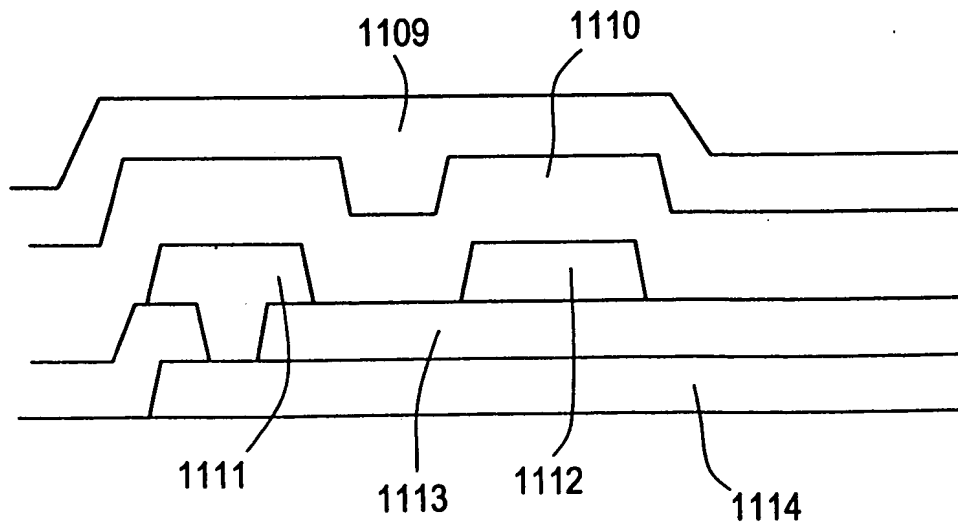


FIG. 12B
PRIOR ART



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TOP SECRET

FIG. 13

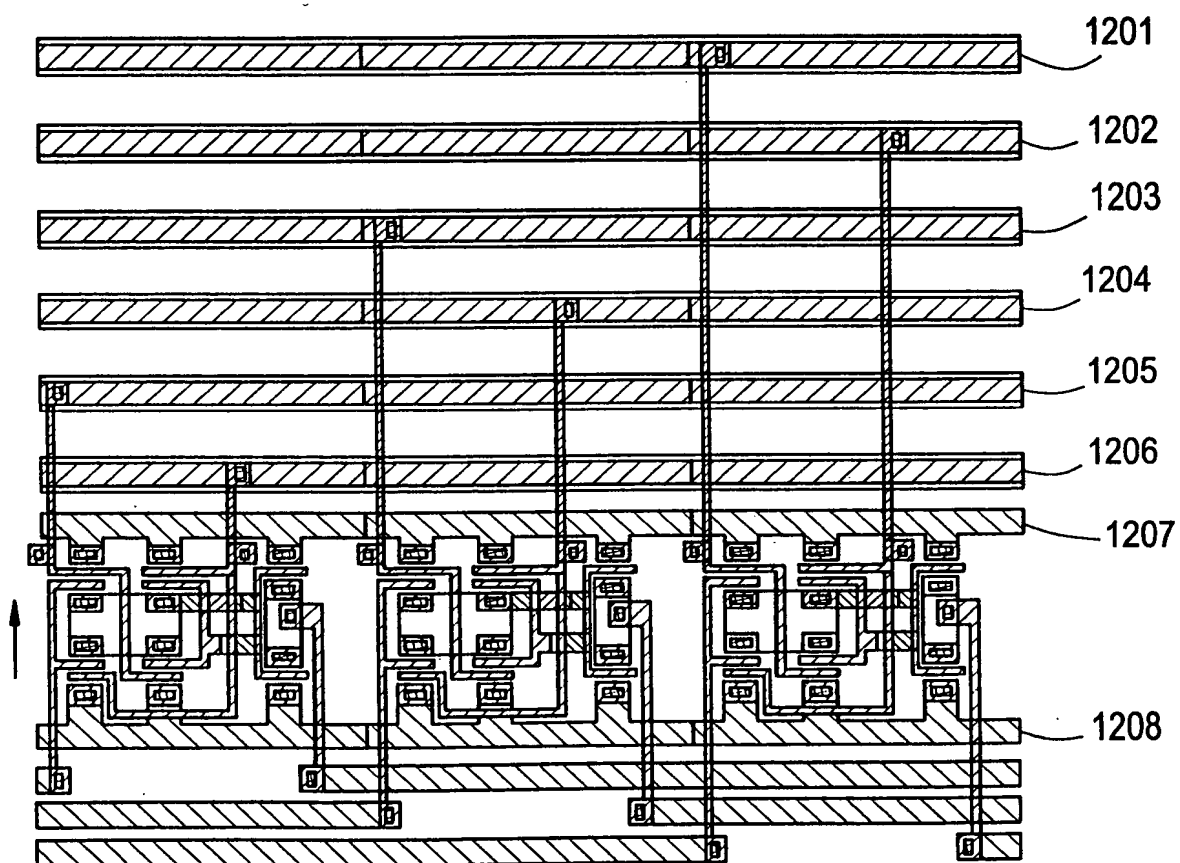


FIG. 14

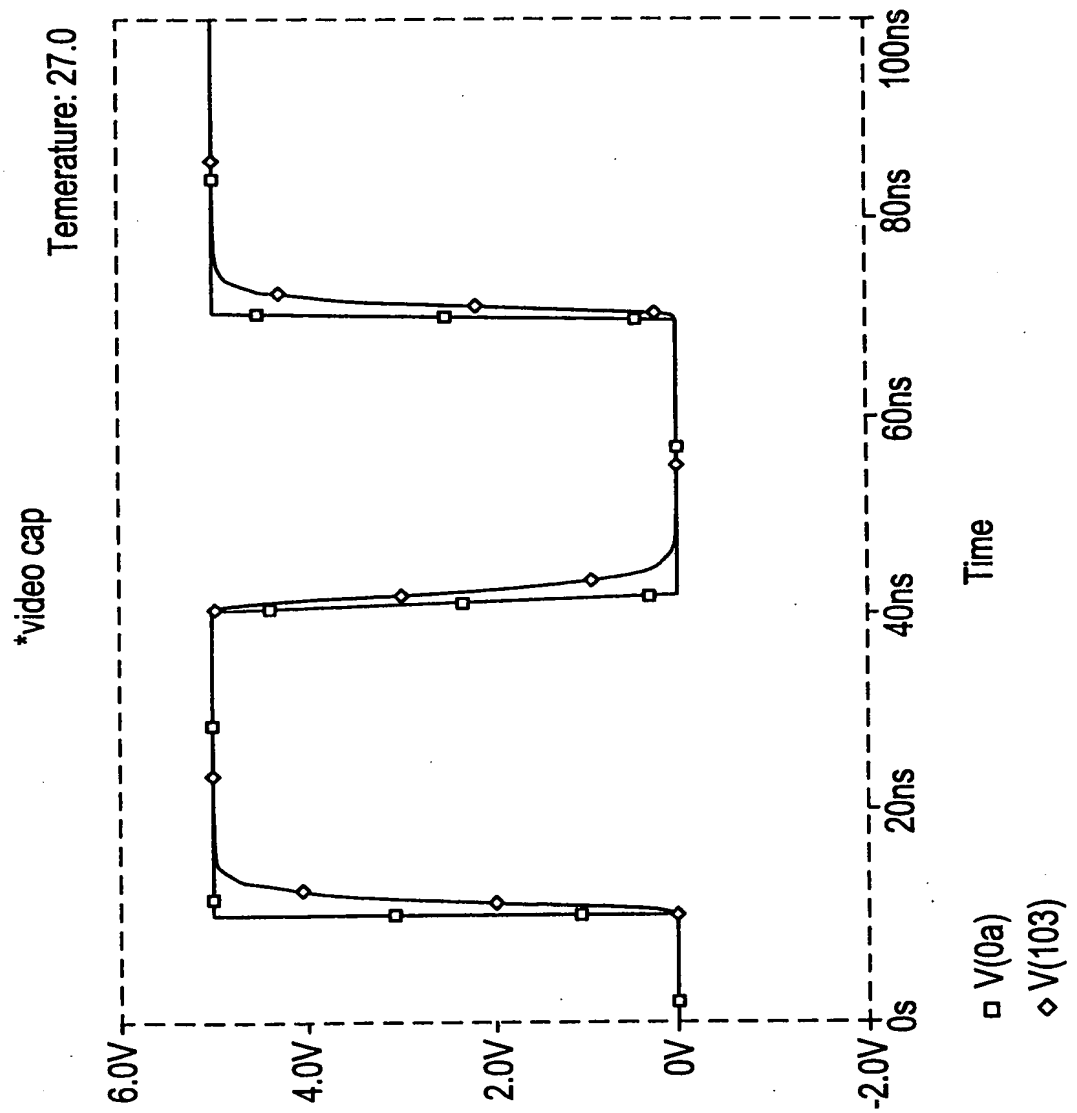


FIG. 15

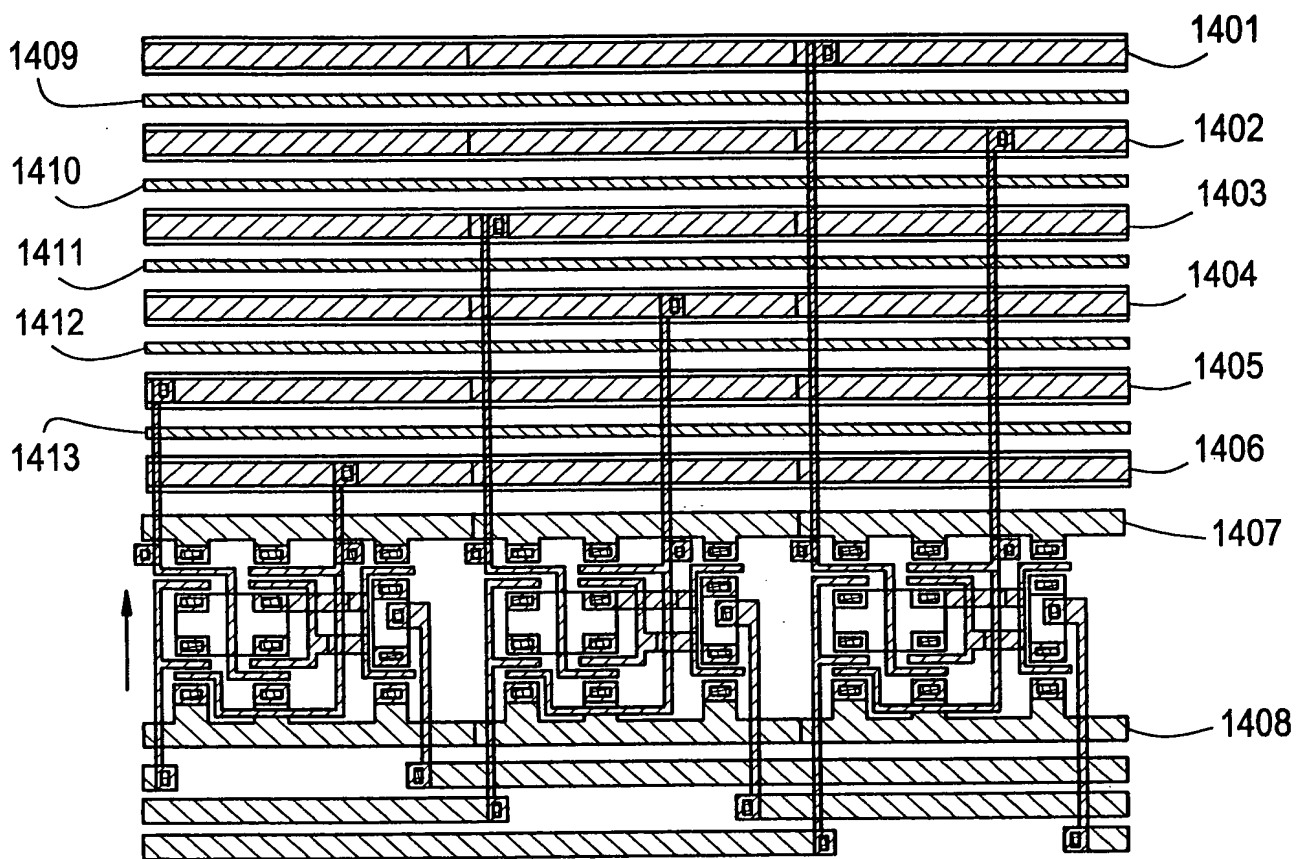
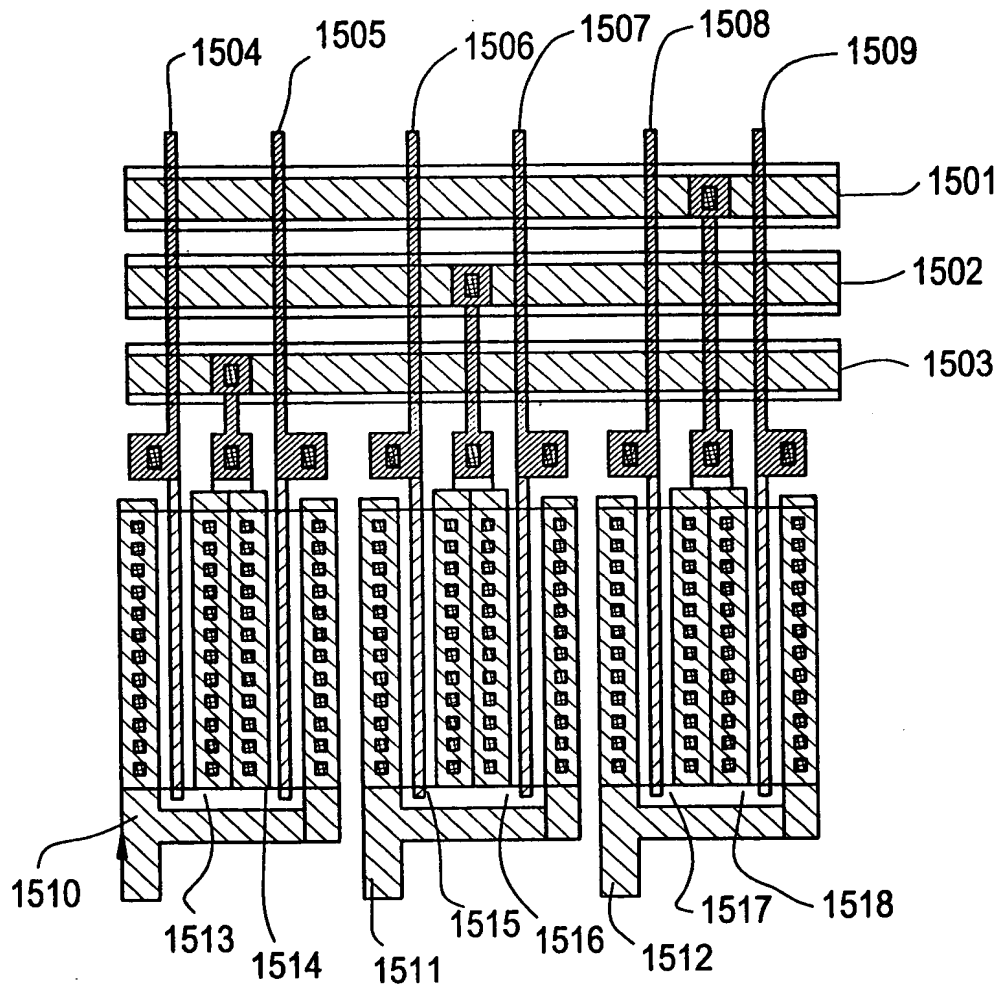
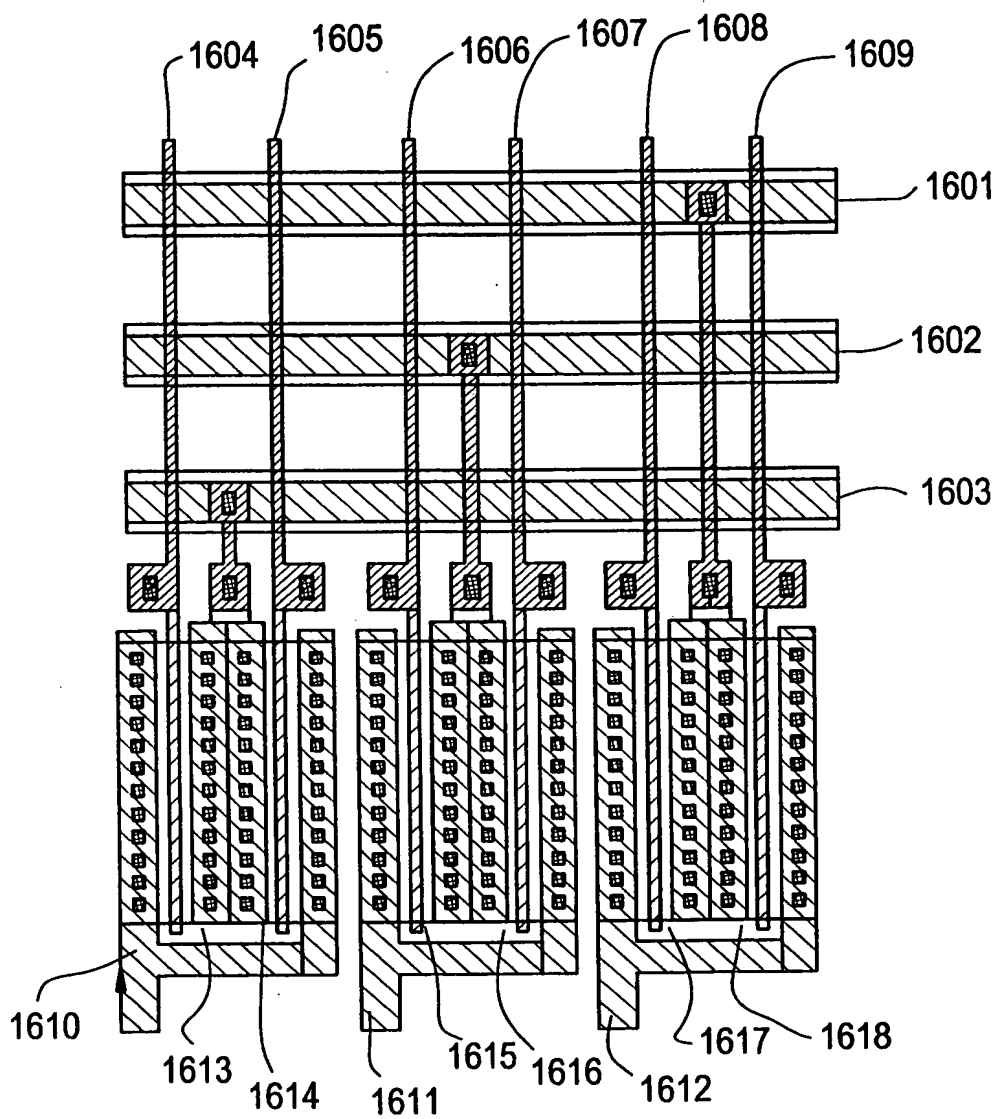


FIG. 16



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FIG. 17



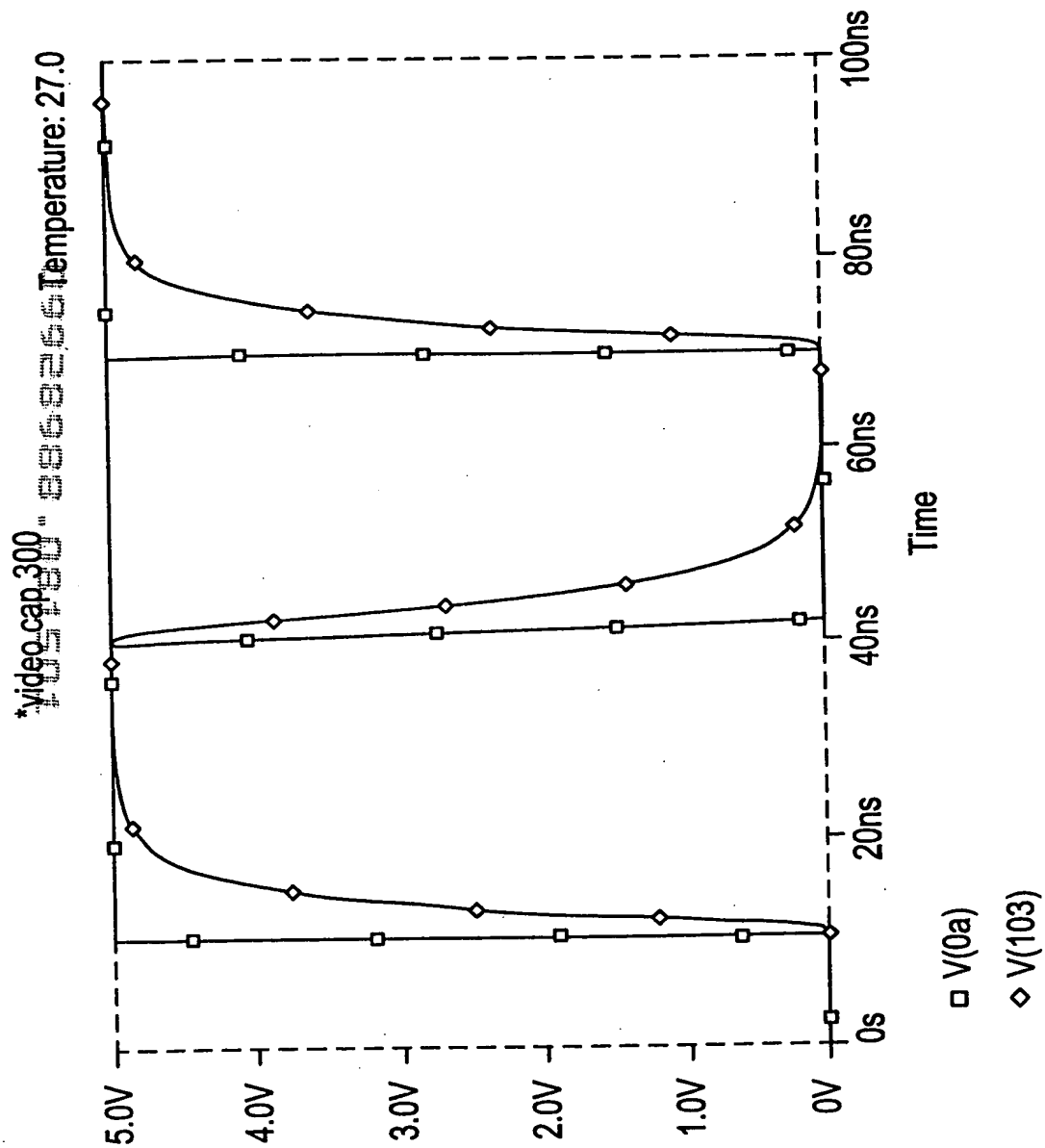


FIG. 19

